

# 1

# ICs

## 1-1. IC Regulators

- Switching Type
- Dropper Type

## 1-2. Stepper Motor Driver ICs

- Unipolar Driver ICs
- Bipolar Driver ICs

## 1-3. Other ICs

- Voltage Doubler/Bridge Rectifier Automatic Switch ICs
- Error Amplifier ICs
- Variable Voltage Detection Type Error Amplifier ICs



## ■Switching Type IC Regulators with Coil

Type No.	Absolute Maximum Ratings (Ta = 25°C)		Recommended Operating Conditions			Electrical Characteristics (Ta = 25°C)			Fig. No.	Remarks			
	DC Input Voltage V <sub>IN</sub> (V)	Storage Temperature T <sub>STG</sub> (°C)	DC Input Voltage Range V <sub>IN</sub> (V)	Output Current Range I <sub>O</sub> (A)	Operating Temperature Top (°C)	Output Voltage V <sub>O</sub> (V)	Efficiency η (%) typ	Switching Frequency f (kHz)					
SI-8201L	45	-25 to +85	10 to 40	0 to 0.4	-10 to +65	5.0 ±0.10	73	25min	6	2-output type Built-in over current protection circuit			
-8202L			11 to 40	0 to 0.35		6.0 ±0.10	74						
-8203L			16 to 40			12.0 ±0.20	79						
-8204L			10 to 40	0 to 0.4		5.2 ±0.10	73		7				
-8221L			8 to 35			5.0 ±0.15	80						
-8211L			15 to 55	0 to 0.3		5.0 ±0.10	63		8				
-8213L			22 to 55			12.0 ±0.20	78						
-8301L			8 to 40	0 to 1	-20 to +85	5.1 ±0.10	73	25typ	9				
-8303L			8.5 to 40			5.4 ±0.10							
-8811L	35		12 to 30	0.05 to 0.45 0 to -0.05	-10 to +70	5.0 ±0.25 -5.0 ±0.25	72	50typ	10				
-8911L	60	24 to 55	0.02 to 0.3 0 to -0.1	-10 to +60	5.0 ±0.25 -5.0 ±0.25	65	68typ						
-8921L		24 to 55			5.1 ±0.1	72							
-8922L		20 to 55	0 to 0.6	-10 to +65	-0.15								
-8401L		35		7 to 33	0 to 0.5	-20 to +85	5.0 ±0.20	80	60typ	7	Built-in over current and thermal protection circuits		
-8402L				15 to 33			12.0 ±0.60						
-8403L				5.3 to 33	0 to 0.5		3.3 ±0.13	75					
-8405L				18 to 33			15.0 ±0.75	89					
-8406L				10 to 33	0 to 0.4		8.0 ±0.40	85					
-8501L				7 to 33			5.0 ±0.20	83		9	Built-in over current and thermal protection Soft start circuits		
-8502L				15 to 33	0 to 0.4		12.0 ±0.60	89					
-8503L				5.3 to 33			3.3 ±0.13	79					
-8504L				12 to 33	0 to 1		9.0 ±0.45	87					
-8505L				18 to 33			15.0 ±0.75	90					



Type No.	Absolute Maximum Ratings (Ta = 25°C)				Electrical Characteristics (Ta = 25°C)				Fig. No.	Remarks
	DC Input Voltage V <sub>IN</sub> (V)	Output Current I <sub>O</sub> (A)	Operating Temperature Top (°C)	Storage Temperature T <sub>STG</sub> (°C)	Recommended Input Voltage V <sub>IN</sub> (V)	Output Voltage V <sub>O</sub> (V)	Input-Output Differential Voltage V <sub>DIF</sub> (V) max	Reset Detection Voltage Level V <sub>OTH/V<sub>O</sub></sub> (%)		
SI-3050R	35	1.5	-30 to +100	-30 to +125	6 to 30	5.0 ± 0.2	1.0	90 to 94	2	Reset function Low power dissipation type Built-in over current, input over voltage and thermal protection circuits

Type No.	Absolute Maximum Ratings (Ta = 25°C)				Electrical Characteristics (Ta = 25°C)				Fig. No.	Remarks
	DC Input Voltage V <sub>IN</sub> (V)	Output Current I <sub>O</sub> (A)	Operating Temperature Top (°C)	Storage Temperature T <sub>STG</sub> (°C)	Output Voltage V <sub>O</sub> (V)	Input-Output Differential Voltage V <sub>DIF</sub> (V) max				
SI-3052V	25	2.0	-20 to +100 (T <sub>c</sub> )	-30 to +125	5.0 ± 0.1	1.0	1.0	1.0	12	Low power dissipation type Built-in over current protection circuits
-3122V	30				12.0 ± 0.2					
-3152V					15.0 ± 0.2					
SI-3052P		2.0	-20 to +80	-30 to +125	5.0 ± 0.1	3.0	3.0	3.0	12	Built-in over current protection circuits
-3122P					12.0 ± 0.2					
-3152P					15.0 ± 0.2					
-3242P					24.0 ± 0.2					
STR9005	25	4.0	-20 to +100 (T <sub>c</sub> )	-30 to +125	5.0 ± 0.1	1.0	1.0	1.0	3	Low power dissipation type Built-in over current protection circuits Output ON/OFF control and fine-adjustment of output voltage possible
9012					12.0 ± 0.2					
9015					15.0 ± 0.2					

## ■3-Output IC Regulators

Type No.	Absolute Maximum Ratings (Ta = 25°C)					Electrical Characteristics (Ta = 25°C)					Fig. No.	Remarks
	DC Input Voltage V <sub>IN</sub> (V)	Output Current I <sub>O</sub> (A)	Power Dissipation (All Output ON) P <sub>D</sub> (W)	Operating Temperature Top (°C)	Storage Temperature T <sub>STG</sub> (°C)	DC Input Voltage Range V <sub>IN</sub> (V)	Output Voltage V <sub>O</sub> (V)	Minimum Input-Output Differential Voltage V <sub>DIF</sub> (V) max	Efficiency η (%) typ	Regulator Type		
SLA 3001M	Reg1	1.5	40	-40 to +125	-30 to +125	13 to 25	12.0 ± 0.48	1.0	—	Dropper type	13	Low power dissipation type Remote sensing possible Built-in overcurrent, input overvoltage and thermal protection circuits
	Reg2					6 to 15	5.0 ± 0.15					
	Reg3					10 to 20	9.0 ± 0.36					
SLA 3002M	Reg1	0.5	37.5	-30 to +85	-40 to +150	7 to 33	5.0 ± 0.25	3.0	80	Switching type	13	Low power dissipation type Remote sensing possible (Reg 2 only) Built-in overcurrent and thermal protection circuits
	Reg2					17 to 30	15.7 ± 0.78	1.0	—	Dropper type		
	Reg3					12 to 33	9.0 ± 0.45	3.0	85	Switching type		
SLA 3004M	Reg1	0.5	0.4	37.5	-40 to +150	7 to 33	5.0 ± 0.25	3.0	80	Switching type	13	Built-in overcurrent and thermal protection circuits
	Reg2					9.0 ± 0.45	9.0 ± 0.36		85			
	Reg3					12 to 33	9.0 ± 0.36		85			

## 1.2 Stepper Motor Driver ICs

### ■ Unipolar Driver ICs

#### ● SLA package product (Heat sink attachable type)

Type No.	Output Current Io (A)	Control Supply Voltage (= Motor Supply Voltage) (V)	Step sequence mode	Package	Fig. No.	Remarks
SLA7026M	3.0 max	46 max	1-2 phase excitation enabled	ZIP18pin	14	Driver + Pulse generator
SLA7024M	1.5 max		2-phase excitation only			
SLA7027MU	1.0 max		Micro-step enabled	ZIP18pin (DIP16pin)	15	
SLA7029M	1.5 max				14	
SLA7022MU	1.0 max				(16)	
SLA7044M (PG001M)	3.0 max				14	
SLA7042M (PG001M)	1.2 max				14 (16) 14 (16)	

#### ● SMA package (Compact type)

Type No.	Output Current Io (A)	Control Supply Voltage (= Motor Supply Voltage) (V)	Step sequence mode	Package	Fig. No.	Remarks
SMA7029M	1.5 max	46 max	2-phase excitation only	ZIP15pin	17	
SMA7022MU	1.0 max					

#### ● SDK package (Surface mount type)

Type No.	Output Current Io (A)	Control Supply Voltage (= Motor Supply Voltage) (V)	Step sequence mode	Package	Fig. No.	Remarks
SDK03M	1.0 max	46 max	1-2 phase excitation enabled	SMD16pin	18	1 motor driven by 2 packages

### ■ Bipolar Driver ICs

Type No.	Output Current Io (A)	Supply Voltage Range (V)	Step sequence mode	Package	Fig. No.	Remarks
SI-7230M	±3.0 max	15 to 45	1-2 phase excitation enabled	SIP20pin	19	Open air package
SI-7502 (SLA5011) (SLA6503)	1.5max *Per 1 phase of motor coil	15 to 45	Pentagonal 4-phase excitation	ZIP27pin (Powder coating) (SIP12pin) (SIP12pin)	20	1 set of 3 products
					21	

## 1-3. Other ICs

### ■ Voltage Doubler/Bridge Rectifier Automatic Switch ICs

Type No.	Absolute Maximum Ratings (Ta = 25°C)					Electrical Characteristics (Ta = 25°C)					Fig. No.	Remarks
	Repetitive Peak Off-state Voltage V <sub>DRM</sub> (V)	RMS On-state Current I <sub>T</sub> (RMS) (A)	Surge On-state Current I <sub>TSM</sub> (A)	Operating Temperature T <sub>OP</sub> (°C)	Storage Temperature T <sub>STG</sub> (°C)	Voltage Doubler Function Turn-on Voltage V <sub>S</sub> (V)	Setting Switchover Voltage V <sub>C1</sub> (V)	V <sub>C2</sub> (V <sub>(AC)</sub> )*	OFF-state Current I <sub>DRM</sub> ( $\mu$ A)	ON-state Voltage V <sub>TH</sub> (V)		
STR 80145A	500	5.0	50	-10 to +100 (T <sub>c</sub> )	-30 to +125	AC80 max	196 ±5	145	100 max	1.8 max	22	
81145A		10.0	100				215 ±5	159				
81159A		5.0	50				205 ±5	145				
STR 82145	500	10.0	100	-20 to +100 (T <sub>c</sub> )	-40 to +125	DC100 max	225 ±5	159	100 max	1.8 max	22	With latching capability
83145												
83159												

\*Reference value

### ■ Error Amplifier ICs (SE series)

Type No.	Absolute Maximum Ratings (Ta = 25°C)			Electrical Characteristics (Ta = 25°C)		Fig. No.	Remarks
	Collector-Ground Voltage V <sub>CEO</sub> (V)	Collector Current I <sub>C</sub> (mA)	Operating Temperature Top (°C)	Output Detection Voltage V <sub>S</sub> (V)			
SE005N	12			5.0 ±0.1			
012N				12.0 ±0.2			
015N				15.0 ±0.2			
024N				24.0 ±0.2			
034N				34.0 ±0.3			
040N				40.0 ±0.4			
070N				70.2 ±0.8			
080N				80.2 ±0.8			
090N				90.0 ±0.8			
105N				105.2 ±0.8			
110N				110.2 ±0.8			
115N				115.2 ±0.8			
120N				120.2 ±0.8			
125N				125.2 ±0.8			
130N				130.2 ±0.8			
135N				135.2 ±0.8			
140N				141.2 ±0.8			

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Low V<sub>S</sub>

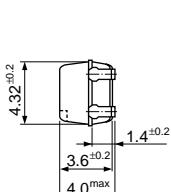
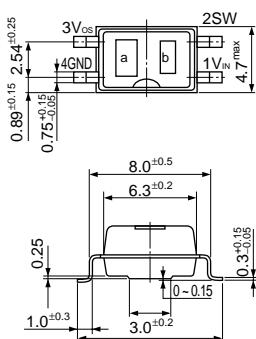
High V<sub>S</sub>

### ■ Variable Voltage Detection Type Error Amplifier ICs

Type No.	Absolute Maximum Ratings (Ta = 25°C)			Electrical Characteristics (Ta = 25°C)		Fig. No.	Remarks
	Collector-Ground Voltage V <sub>CEO</sub> (V)	Collector Current I <sub>C</sub> (mA)	Operating Temperature Top (°C)	Output Detection Voltage V <sub>S</sub> (V)			
SE-B3	150	20	-20 to +125 (T <sub>c</sub> )	141.2 ±0.6 Condition (I <sub>IN</sub> = 4mA, R <sub>C</sub> = 9.1kΩ) (R <sub>S</sub> = 88.7kΩ)		23	Variable voltage detection Gain adjustment possible

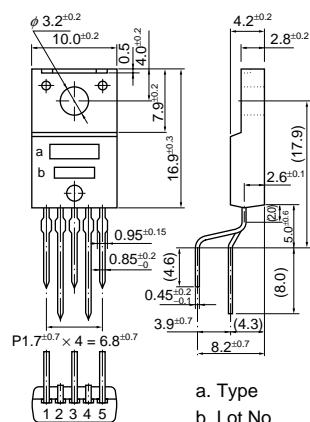
## ● External Dimensions (unit: mm)

Fig. 1



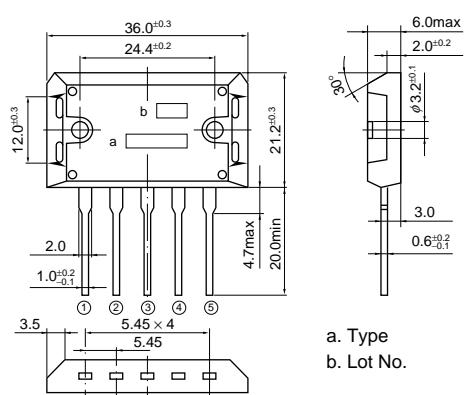
a. Type  
b. Lot No.

Fig. 2



a. Type  
b. Lot No.

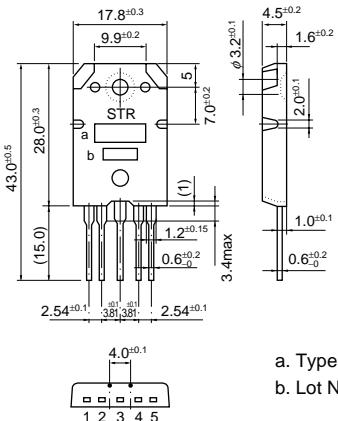
Fig. 3



a. Type  
b. Lot No.

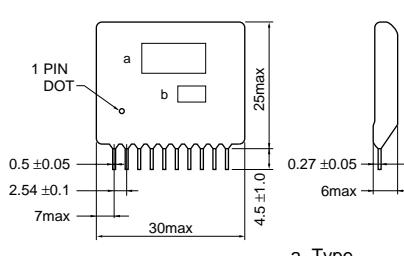
Forming No. 1101

Fig. 4



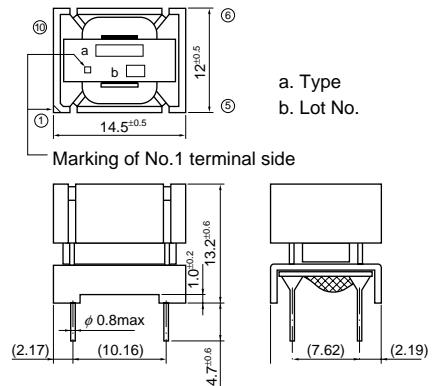
a. Type  
b. Lot No.

Fig. 5



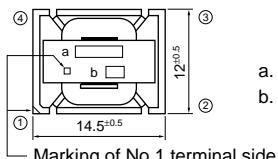
a. Type  
b. Lot No.

Fig. 6



a. Type  
b. Lot No.

Fig. 7



a. Type  
b. Lot No.

Marking of No.1 terminal side

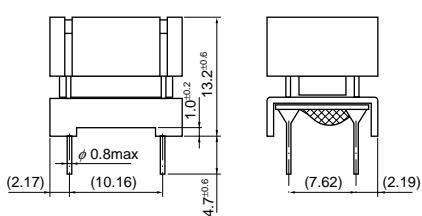
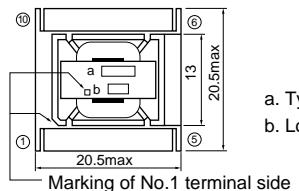


Fig. 8



a. Type  
b. Lot No.

Marking of No.1 terminal side

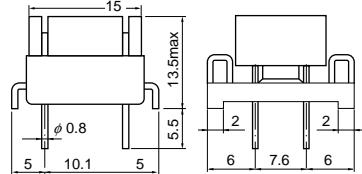
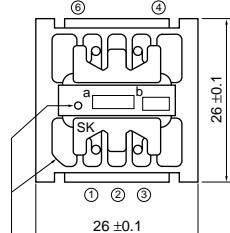


Fig. 9



a. Type  
b. Lot No.

Marking of No.1 terminal side

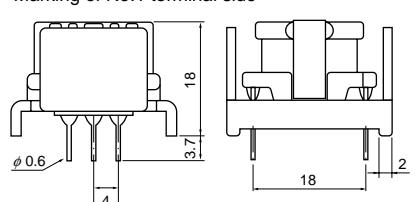
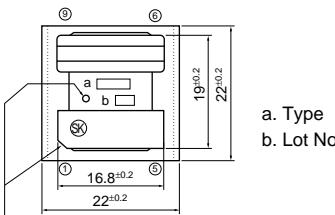


Fig. 10



a. Type  
b. Lot No.

Marking of No.1 terminal side

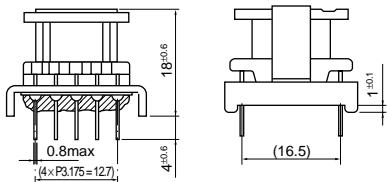
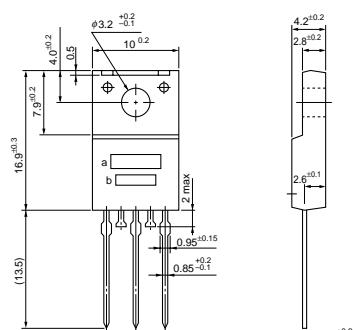


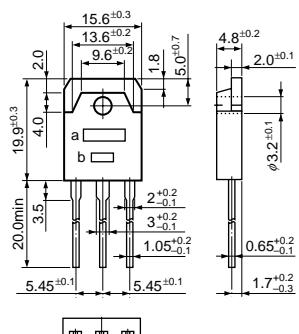
Fig. 11



a. Type  
b. Lot No.

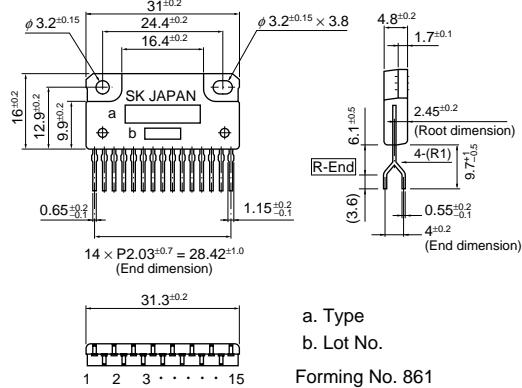
Terminal layout  
1 GND  
2 VOUT  
3 VIN

Fig. 12



a. Type  
b. Lot No.

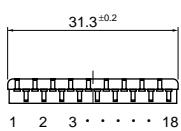
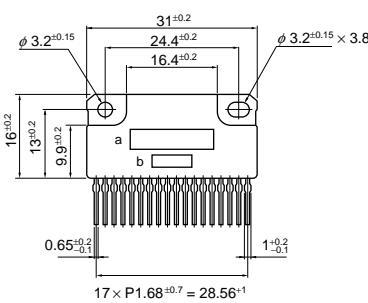
Fig. 13



a. Type  
b. Lot No.

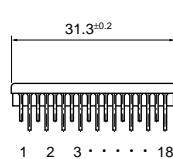
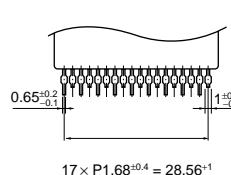
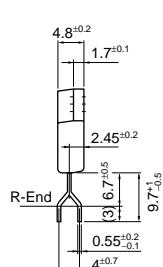
Forming No. 861

Fig. 14



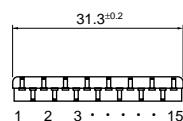
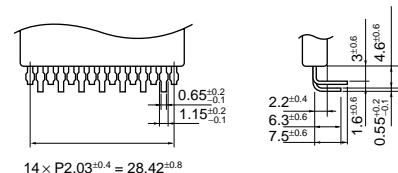
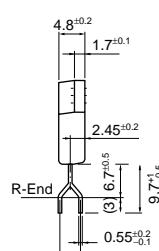
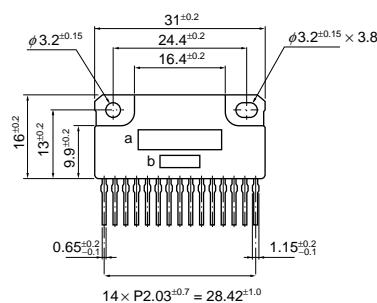
a. Type  
b. Lot No.

Forming No. 871



Forming No. 872

Fig. 15



Forming No. 853

a. Type  
b. Lot No.



Forming No. 855

Fig. 16

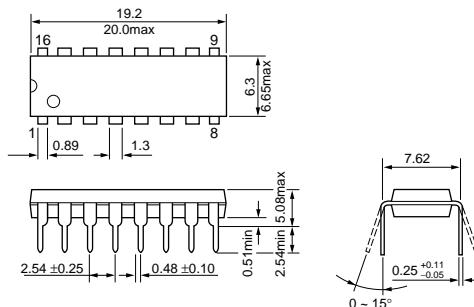
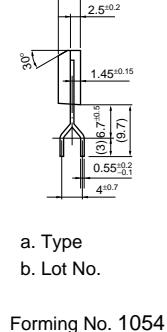
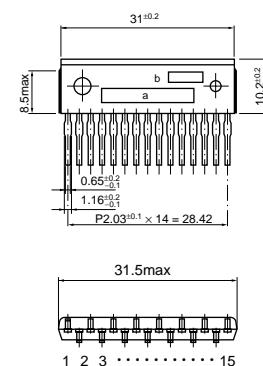


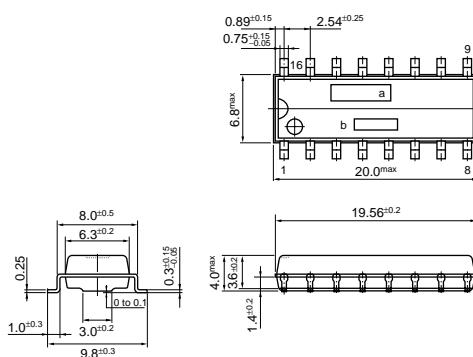
Fig. 17



a. Type  
b. Lot No.

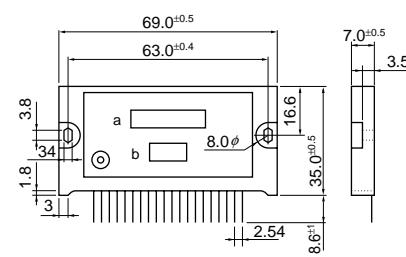
Forming No. 1054

Fig. 18



a. Type  
b. Lot No.

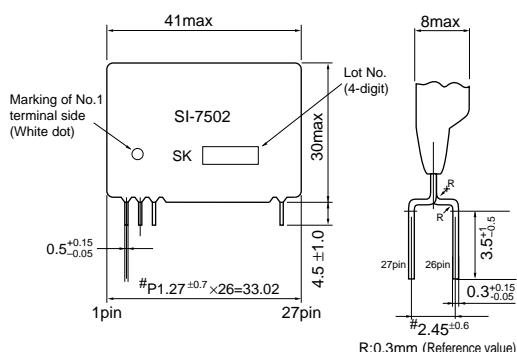
Fig. 19



Terminal No.1 ..... 20

a. Type  
b. Lot No.

Fig. 20



Note) Symbol # indicates the dimension of the lead end.

Fig. 21

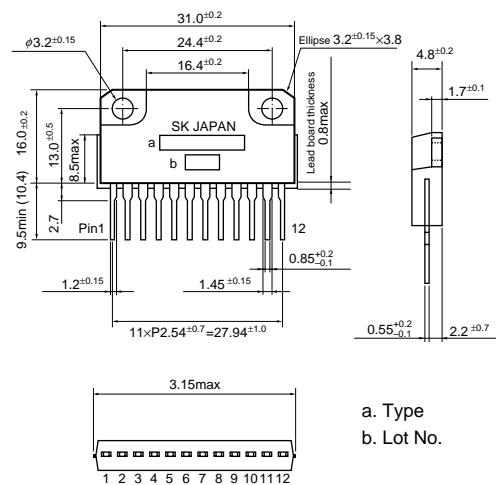


Fig. 22

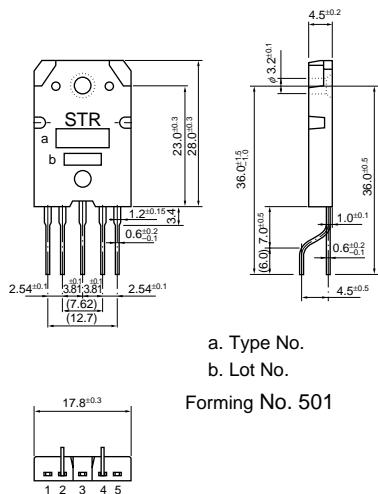
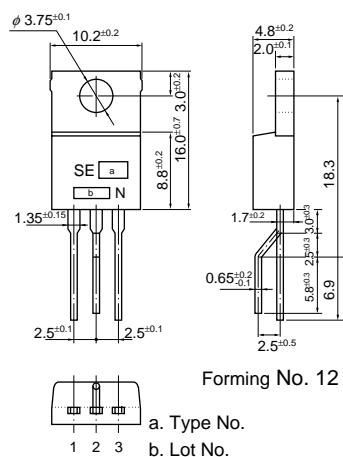
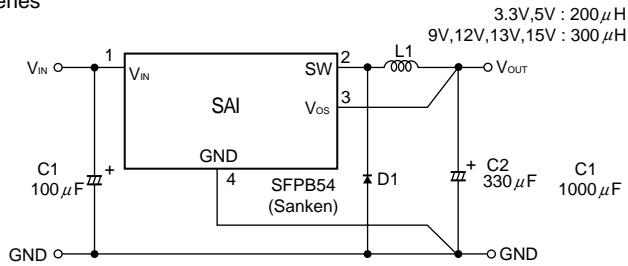


Fig. 23 MT-25 (TO-220)

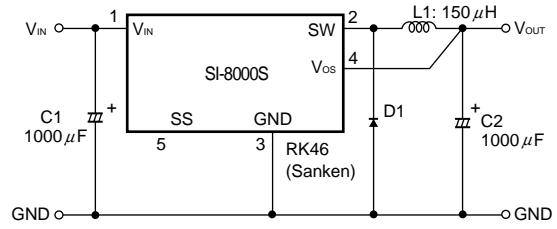


## ● Standard Connection Diagrams

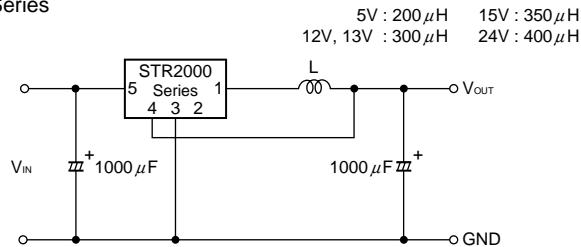
SAI Series



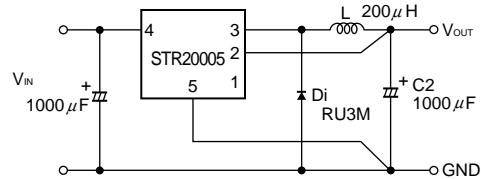
SI-8000S Series



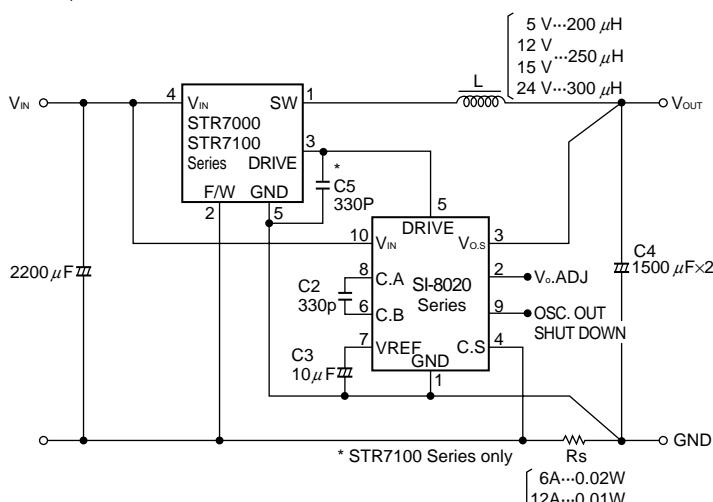
STR2000 Series



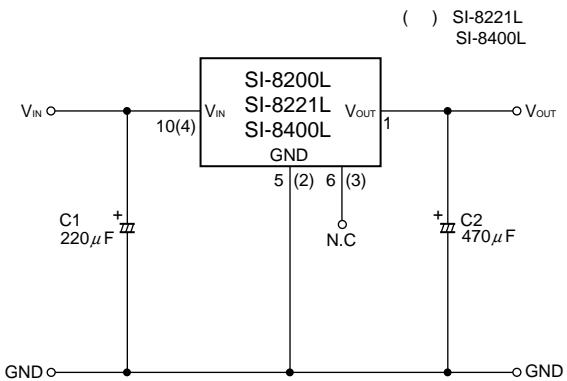
STR20005



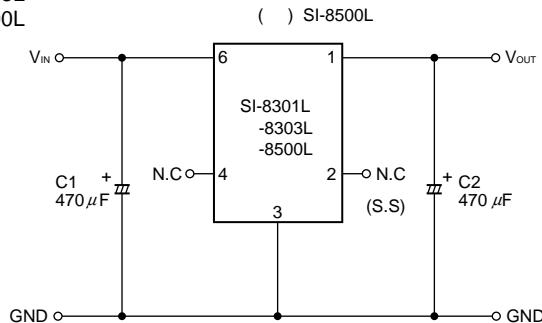
STR7000, 7100/SI-8020 Series



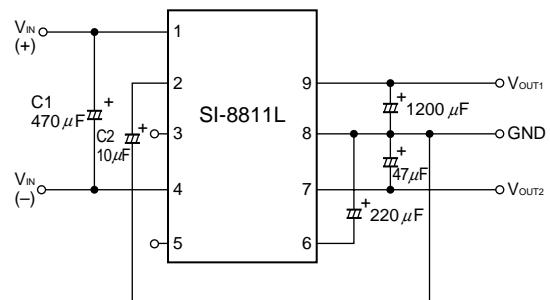
SI-8200L/8400L Series



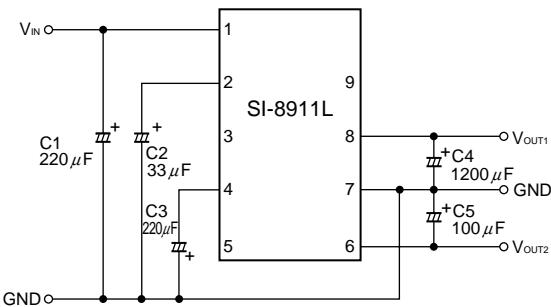
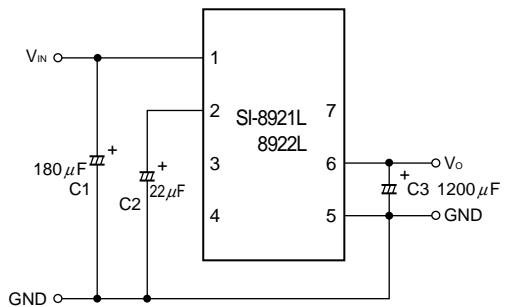
SI-8301L  
-8303L  
-8500L



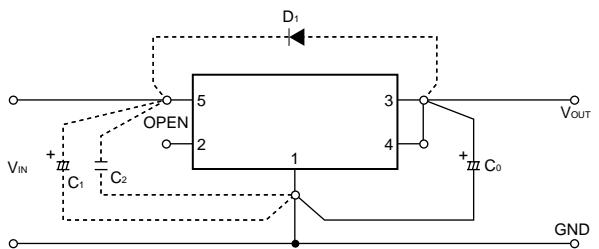
SI-8811L



SI-8911L

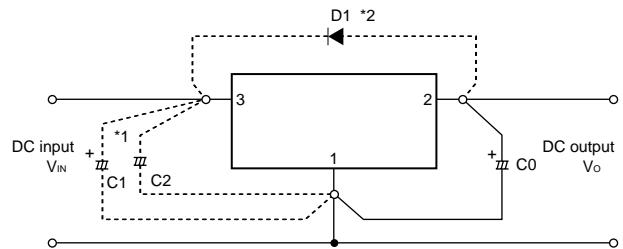
SI-8921L  
8922L

SI-3000F/3000C/3000J Series



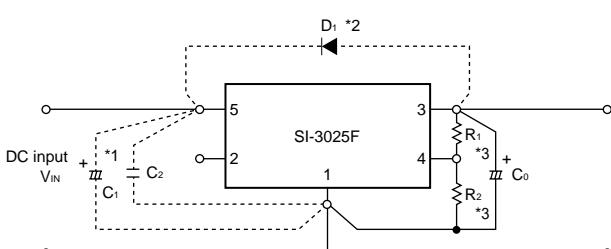
$C_0$  : Output capacitor 47 to 100  $\mu\text{F}$   
 \*1  $C_1, C_2$  : Oscillation prevention capacitor ( $C_1$  : Approx. 47  $\mu\text{F}$ ,  $C_2$  : Approx. 0.33  $\mu\text{F}$ )  
 \*2  $D_1$  : Diode for protection against the occurrence of a reverse bias condition between the input and the output.

SI-3000N/3001N/3002N Series



$C_0$  : Output capacitor (47 to 100  $\mu\text{F}$ , 50V)  
 \*1  $C_1$  : Oscillation prevention capacitor ( $C_1$  : Approx. 47  $\mu\text{F}$ ,  
 $C_2$  : Approx. 0.33  $\mu\text{F}$ )  
 \*2  $D_1$  : Diode for protection against the occurrence of a reverse bias condition between the input and the output.

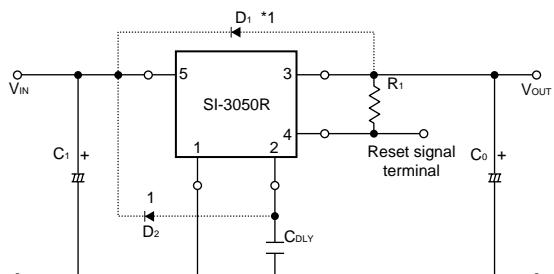
SI-3025F



$C_0$  : Output capacitor (47 to 100  $\mu\text{F}$ , 50V)  
 \*1  $C_1, C_2$  : Oscillation prevention capacitor ( $C_1$  : Approx. 47  $\mu\text{F}$ ,  
 $C_2$  : Approx. 0.33  $\mu\text{F}$ )  
 \*2  $D_1$  : Diode for protection against the occurrence of a reverse bias condition between the input and the output.  
 \*3  $R_1, R_2$  : External resistor for output voltage setting.  
 The equation (1) is applied to output voltage  $V_O$  and external resistor  $R_1$  &  $R_2$ .  

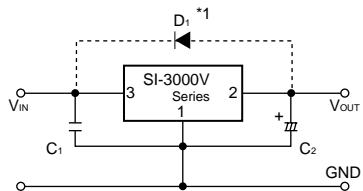
$$V_O = V_{REF} \cdot \left(1 + \frac{R_1}{R_2}\right) \quad (1) \quad (V_{REF} = 2.55V \text{ (typ.)})$$
  
 $R_2$  should be 2.55 k $\Omega$  for stable operation.

SI-3050R



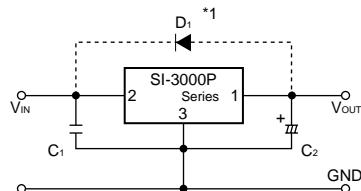
$C_0$  : Output capacitor (Approx. 200  $\mu\text{F}$ , 50V)  
 $C_1$  : Oscillation prevention capacitor ( $C_1$  : Approx. 47  $\mu\text{F}$ )  
 Wirings between 5 ( $V_{IN}$ ) and 1 (GND) terminals should be the shortest distance as much as possible. When the input line is inductive or if a long wire is used, add a capacitor with good high frequency characteristics and a capacitance of Approx. 0.33  $\mu\text{F}$  in parallel with the  $C_1$ . Tantalum capacitors are recommended for  $C_1$  and  $C_0$  when operating in low temperature environments.  
 $C_{DLY}$  : Delay capacitor (reset output)  
 $R_1$  : Pull-up resistor (more than 300  $\Omega$ )  
 \*1  $D_1, D_2$  : Diode for protection against the occurrence of a reverse bias condition between the input and the output.

SI-3000V



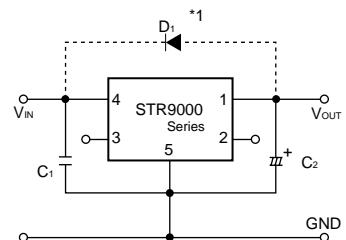
C<sub>1</sub>: Oscillation prevention capacitor (0.33 μF)  
 C<sub>2</sub>: Output capacitor (47 to 100 μF)  
 \*1 D<sub>1</sub>: Diode for protection against the occurrence of a reverse bias condition between the input and the output.

SI-3000P



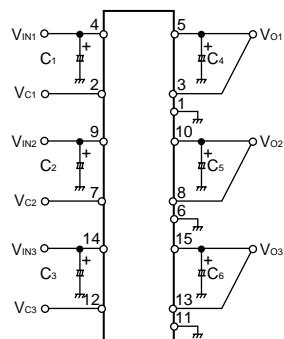
C<sub>1</sub>: Oscillation prevention capacitor (0.33 μF)  
 C<sub>2</sub>: Output capacitor (47 to 100 μF)  
 \*1 D<sub>1</sub>: Diode for protection against the occurrence of a reverse bias condition between the input and the output.

STR9000

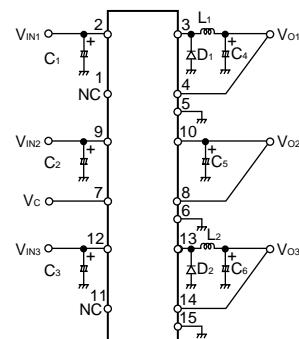


C<sub>1</sub>: Oscillation prevention capacitor (0.33 μF)  
 C<sub>2</sub>: Output capacitor (47 to 100 μF)  
 \*1 D<sub>1</sub>: Diode for protection against the occurrence of a reverse bias condition between the input and the output.

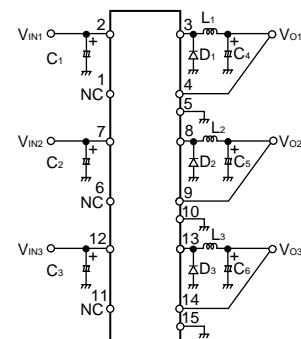
SLA3001M



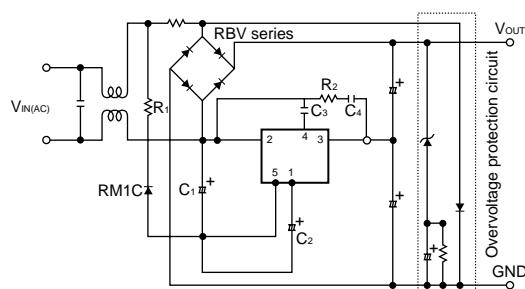
SLA3002M



SLA3004M

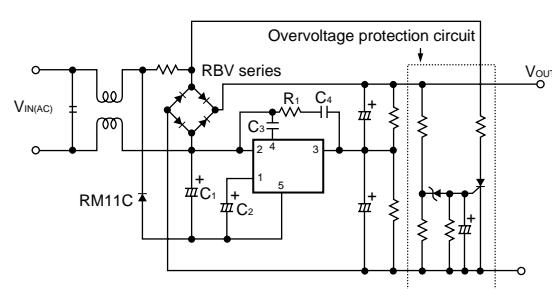


STR80000A Example of application circuit



R<sub>1</sub>: 2.2Ω R<sub>2</sub>: 4.7Ω  
 C<sub>1</sub>: 2.2 μF/400V C<sub>2</sub>: 100 μF/10V C<sub>3</sub>: 0.1 μF  
 C<sub>4</sub>: 0.047 μF

STR82000/83000 Example of application circuit



C<sub>1</sub>: 4.7 μF/400V, C<sub>2</sub>: 1 μF/50V, C<sub>3</sub>: 0.047 μF/50V  
 C<sub>4</sub>: 0.047 μF/250V, R<sub>1</sub>: 4.7Ω